Working Group 10.5 (Design and Engineering of Electronic Systems) Yearly report for the period August 2021 – August 2023

Written by Graziano Pravadelli

The WG 10.5 home page is located at: https://cisd.di.univr.it/wg10-5/

1 - Members

One new member has been added during the period:

Prof. Florenc Demrozi, University of Stavanger, Norway
 Expertise: Internet of (medical) things, human activity recognition, virtual coaching, telemedicine.

Then, there are in total 59 members and 10 honorary members.

The geographic distribution of the members is as follows:

- Europe: 37 members, 4 honorary members
- North America: 9 members, 3 honorary members
- South America: 3 members, 1 honorary member
- Middle East 2 members
- Asia 8 members, 2 honorary members

2 - Officers

Since October 2020 the WG chair is Prof. Graziano Pravadelli, University of Verona, Italy

Since March 2021 the WG vice chairs are:

- Prof. Ibrahim Elfadel, Masdar Institute, United Arab Emirates
- Prof. Ricardo Reis, Universidade Federal do Rio Grande do Sul, Brazil
- Dr. Victor Grimblatt, Synopsys, Chile
- Prof. Tiziana Margaria, University of Limerick, Ireland

Their roles of the WG chairs are as follows:

Name	Geographical area	Main expertise	Promotional role
Tiziana Margaria	Europe	Formal methods	Women enrolment
Ibrahim Elfadel	Africa, Asia and Far East	Cyber physical systems and IoT	Mentoring for VLSI- SOC organizers
Ricardo Reis	America	Physical design and testing	Networking with IFIP initiatives
Victor Grimblatt	Worldwide	EDA, electronic industry	Industrial liaison

3 - Meetings

The following business meetings took place in the period August 2022 to July 2023:

- October 2, 2022, hybrid, co-located with VLSI-SOC 22 in Patras, 10 attendees.
- April 18, 2022, hybrid, co-located with DATE 23, 19 attendees.

All meeting minutes are available at: https://cisd.di.univr.it/wg10-5/meetings.html

The following meetings are currently planned for the coming year:

 October 15, physical meeting in Dubai (UAE) during VLSI-SOC 2023, with possibility of remotely connect for people that will not attend the conference.

4 - Sponsored Events

The main conference sponsorship activity of WG 10.5 is the annual IFIP/IEEE International Conference on Very Large Scale Integration, VLSI-SOC Conference.

The members of the Steering Committee are:

Matthew Guthaus (UC Santa Cruz, USA), Luis Miguel Silveira (INESC ID, PT), Chi-Ying Tsui (HKUST, Hong Kong, CN), Manfred Glesner (TU Darmstadt, DE), Ibrahim Elfadel (Masdar Institute of Science and Technology, UAE), Fatih Ugurdag (Ozyegin University, TR), Ricardo Reis (UFRGS, BR), Ian O'Connor (ECL, FR), Masahiro Fujita (The University of Tokyo, JP), Salvador Mir (TIMA, FR), Graziano Pravadelli (University of Verona, IT)

VLSI-SoC 2022

The 30th VLSI-SOC conference from October 3 to 5, 2022, was co-sponsored by IFIP, IEEE CEDA, and IEEE CASS.

Its venue was Patras, Grece, finally in presence, after two editions run remotely due to the covid pandemic.

The conference theme was "SoCs for 5G Evolution and 6G". The Conference was organized and chaired by Odysseas Koufopavlou (University of Patras, Greece) and Graziano Pravadelli (University of Verona, Italy). The Program co-chairs were Vassilis Paliouras (University of Patras, Greece) and Lech Jozwiak (Eindhoven University of Technology, The Netherlands).

There were 3 keynotes, 11 regular sessions with 42 technical presentations, 1 poster sessions with 13 contributions, 1 PhD forum with 10 presentations, 1 student forum with 7 presentations, and 5 special sessions with 17 contributions.

Events co-sponsored by WG 10.5 without financial commitment

- **SBCCI 2023** 35th Symposium on Integrated Circuits and Systems Design, August 28-Sept 1, 2023, Rio de Janeiro, Brazil.
- **FDL 2023** Forum on Specification and Design Languages, September 13-15, 2023, Turin, Italy.

Planned Events

The following main sponsored events are already planned:

- **IFIP/IEEE VLSI-SoC 2023:** Dubai, UAE, 16-18 October 2023, General Chairs: Prof. Fadi Aloul (American University of Sharjah, UAE), Luis Muguel Silveira (INESC ID/IST, Portugal).
- IFIP/IEEE VLSI-SoC 2024: Tangier, Morocco, October 2024, General Chairs: Prof. Said Hamdioui (TU Delft, NL) and Prof. Anass El Haddadi (Abdelmalek Essaadi University)

The following events are actually considered to be sponsored without any financial implication:

• **SBCCI 2024:** August, 2024.

5 – Other initiatives

- The following members of WG 10.5 were nominated by the IFIP 10.5 award committee for the IFIP service award to the IFIP General assembly in July 2023:
 - o Graziano Pravadelli (University of Verona, Italy).